Substitute Form PTO-1449 (Modified) U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-601001	Application No. 09/942,116
Information Disclosure Statement by Applicant (Use several sheets if necessary) DEC 2 9 2003 (37 CFR §1.98(b))	Applicant **William R. Wheeler et al.**	
	Filing Date **August 29, 2001**	Group Art Unit **2185**

U.S. Patent Documents

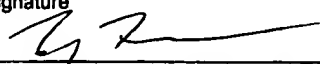| Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
✓	AA	5,603,015	02/1997	Kurosawa, et al.			
✓	AB	5,604,894	02/1997	Pickens, et al.			
✓	AC	5,694,579	12/1997	Razdan, et al.			
✓	AD	5,706,476	01/1998	Giramma			**RECEIVED**
	AE						
	AF						JAN 0 2 2004
	AG						Technology Center 2100
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation Yes	No
✓	AL	0 404 482	12/1990	EPO				
	AM							
	AN							
	AO							
	AP							

Other Documents (Include Author, Title, Date, and Place of Publication)
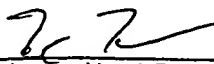
Examiner Initial	Desig. ID	Document
✓	AQ	Parlakbilek, et al., "A Multiple-Strength Multiple-Delay Compiled-Code Logic Simulator", *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems*, 12(12):1937-1946 (1993).
	AR	
	AS	
	AT	

Examiner Signature	Date Considered 10/12/0

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Substitute Disclosure Form (PTO-1449)

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-601001	Application No. 09/942,116
Information Discl sure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant William R. Wheeler et al.	
		Filing Date August 29, 2001	Group Art Unit 2185

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
ʒ	AA	RE 38,059	04/2003	Yano, et al.			
ʒ	AB	4,703,435	10/1987	Darringer, et al.			
ʒ	AC	4,970,664	11/1990	Kaiser, et al.			
ʒ	AD	5,212,650	05/1993	Hooper, et al.			
ʒ	AE	5,267,175	11/1993	Hooper			
ʒ	AF	5,278,769	01/1994	Bair, et al.			
ʒ	AG	5,287,289	02/1994	Kageyama, et al.			
ʒ	AH	5,297,053	03/1994	Pease, et al.			
ʒ	AI	5,301,318	04/1994	Mittal			
ʒ	AJ	5,384,710	01/1995	Lam, et al.			
ʒ	AK	5,475,605	12/1995	Lin			
ʒ	AL	5,493,507	02/1996	Shinde, et al.			
ʒ	AM	5,544,067	08/1996	Rostoker, et al.			
ʒ	AN	5,568,397	10/1996	Yamashita, et al.			
ʒ	AO	5,598,347	01/1997	Iwasaki			
ʒ	AP	5,663,662	09/1997	Kurosawa			
ʒ	AQ	5,673,198	09/1997	Lawman, et al.			
ʒ	AR	5,685,006	11/1997	Shiraishi			
ʒ	AS	5,717,928	02/1998	Campmas, et al.			
ʒ	AT	5,724,250	03/1998	Kerzman, et al.			
ʒ	AU	5,757,655	05/1998	Shih, et al.			
ʒ	AV	5,809,283	09/1998	Vaidyanathan, et al.			
ʒ	AW	5,831,869	11/1998	Ellis, et al.			
ʒ	AX	5,841,663	11/1998	Sharma, et al.			
ʒ	AY	5,892,682	04/1999	Hasley, et al.			
ʒ	AZ	5,903,469	05/1999	Ho			
ʒ	AAA	5,937,190	08/1999	Gregory, et al.			

RECEIVED

MAR 1 7 2004
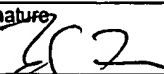
Technology Center 2100

Examiner Signature	Date Considered
	10/12/05

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Substitute Form PTO-1449 (Modified)		U.S. Department of Commerce Patent and Trademark Office		Attorney's Docket No. 10559-601001		Application No. 09/942,116
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))				Applicant William R. Wheeler et al.		
				Filing Date August 29, 2001		Group Art Unit 2185

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
ʒ	ABB	5,974,242	10/1999	Damarla, et al.			
⁊	ACC	6,077,304	06/2000	Kasuya			
ʒ	ADD	6,161,211	12/2000	Southgate			
⁊	AEE	6,178,541	01/2001	Joly, et al.			
⁊	AFF	6,208,954	03/2001	Houtchens			
⁊	AGG	6,216,256	04/2001	Inoue, et al.			
⁊	AHH	6,226,780	05/2001	Bahra, et al.			
⁊	AII	6,234,658	05/2001	Houldsworth			
ʒ	AJJ	6,275,973	08/2001	Wein			
⁊	AKK	6,292,931	09/2001	Dupenloup			
⁊	ALL	6,327,693	12/2001	Cheng, et al.			
⁊	AMM	6,353,806	03/2002	Gehlot			
⁊	ANN	6,353,915	03/2002	Deal, et al.			
ʒ	AOO	6,360,356	03/2002	Eng			
⁊	APP	6,381,563	04/2002	O'Riordan, et al.			
⁊	AQQ	6,381,565	04/2002	Nakamura			
⁊	ARR	6,421,816	07/2002	Ishikura			
⁊	ASS	6,438,729	08/2002	Ho			
⁊	ATT	6,438,731	08/2002	Segal			
⁊	AUU	6,440,780	08/2002	Kimura, et al.			
⁊	AVV	6,473,885	10/2002	Wallace			
⁊	AWW	6,477,688	11/2002	Wallace			
⁊	AXX	6,490,545	12/2002	Peng			
⁊	AYY	6,505,328	01/2003	Van Ginneken, et al.			
ʒ	AZZ	6,516,456	02/2003	Garnett, et al.			
⁊	AAAA	6,519,742	02/2003	Falk			
⁊	ABBB	6,523,156	02/2003	Cirit			

RECEIVED

MAR 1 7 2004

Technology Center 2100

Examiner Signature	Date Considered
	10/12/05

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Substitute Disclosure Form (PTO-1449)

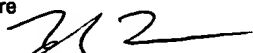| Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-601001	Application No. 09/942,116
Information Disclosure Statement by Applicant (Use several sheets if necessary) MAR 15 2004 (37 CFR §1.98(b))		Applicant William R. Wheeler et al.	
		Filing Date August 29, 2001	Group Art Unit 2185

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
₹	ACCC	6,539,536	03/2003	Singh et al.			
₹	ADDD	6,546,528	04/2003	Sasaki, et al.			
₹	AEEE	6,574,787	06/2003	Anderson			
₹	AFFF	6,591,407	07/2003	Kaufman, et al.			
₹	AGGG	2002/0038447	03/2002	Kim, et al.			
₹	AHHH	2002/0059054	05/2002	Bade, et al.			
₹	AIII	2002/0112221	08/2002	Ferreri, et al.			
₹	AJJJ	2002/0138244	09/2002	Meyer			
₹	AKKK	2003/0004699	01/2003	Choi, et al.			
₹	ALLL	2003/0036871	02/2003	Fuller, et al.			
₹	AMMM	2003/0177455	09/2003	Kaufman, et al.			

RECEIVED

MAR 17 2004

Technology Center 2100

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation Yes	Translation No
₹	ANNN	0 404 482	12/1990	EPO				
₹	AOOO	0 433 066	06/1991	EPO				
₹	APPP	0 720 233	07/1996	EPO				
₹	AQQQ	0 901 088	03/1999	EPO				
₹	ARRR	1 065 611	01/2001	EPO				
₹	ASSS	58-060559	04/1983	Japan				✓
₹	ATTT	03-225523	10/1991	Japan				✓
₹	AUUU	07-049890	02/1995	Japan				✓
₹	AVVV	08-314892	11/1996	Japan				✓
₹	AWWW	2001-068994	03/2001	Japan				✓
₹	AXXX	WO 98/37475	08/1998	WIPO				
₹	AYYY	WO 98/55879	12/1998	WIPO				
₹	AZZZ	WO 99/39268	08/1999	WIPO				

ABSTRACT only
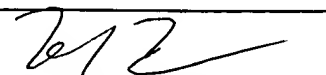
Examiner Signature 212	Date Considered 10/12/05

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-601001	Application No. 09/942,11 RECEIVED
Information Disclosure Statement by Applicant (Use several sheets if necessary) MAR 15 2004 (37 CFR §1.98(b))		Applicant William R. Wheeler et al.	MAR 1 7 2004
		Filing Date August 29, 2001	Group Art Unit 2185 Technology Center 2100

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation Yes	No
⁊	AAAAA	WO 00/65492	11/2000	WIPO				

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
⁊	ABBBB	Gassenfeit, E. H., "Control System Design Realization via VHDL-A: Requirements", Proceedings of the 1996 IEEE International Symposium on Computer-Aided Control System Design, September 15, 1996, pp. 282-285.
⁊	ACCCC	Kutzschebauch, "Efficient logic optimization using regularity extraction", Proceedings of 2000 International Conference on Computer Design, September 17, 2000, pp. 487-493.
⁊	ADDDD	Lahti, et al., "SADE: a Graphical Toll for VHDL-Based System Analysis", 1991 IEEE International Conference on Computer-Aided Design, November 11, 1991, pp. 262-265.
⁊	AEEEE	Lin, et al., "A Goal Tree Based High-Level Test Planning System for DSP Real Number Models", 1998 Proceedings of International Test Conference, October 18, 1998, pp. 1000-1009.
⁊	AFFFF	Maxfield, C., "Digital Logic Simulation: Event-Driven, Cycle-Based, and Home-Brewed", *Electrical Design News*, 41(14):129-136 (1996).
⁊	AGGGG	NB84035598, "Binary Relational Schema to Model Structured LSI Design", IBM Technical Disclosure Bulletin, Vol. 26, No. 10B, March 1984, pp. 5598-5601.
⁊	AHHHH	NN7807629, "Functional Oriented Symbolic Macromodeling Algorithm", IBM Technical Disclosure Bulletin, Vol. 21, No. 2, July 1978, pp. 629-631.
⁊	AIIII	NN8006341, "Macro Physical-To-Logical Checking LSI Chip Design", IBM Technical Disclosure Bulletin, Vol. 23, No. 1, June 1980, pp. 341-345.
⁊	AJJJJ	NN9407481, "Functional Modeling using object Collaboration Diagram", IBM Technical Disclosure Bulletin, Vol. 37, No. 7, July 1994, pp. 481-486.
⁊	AKKKK	Su, Stephen, "An Interactive Design Automation System", *Proceedings of the 10th Design Automation Workshop on Design Automation*, pp. 253-261, June 1973
⁊	ALLLL	Yli-Pietila, et al., "The Design and Simulation of Complex Multitechnology Systems", IEEE International Conference on Systems Engineering, August 9, 1990, pp. 474-477.

Examiner Signature	Date Considered
	10/12/05

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.